

Fully Associative Cache

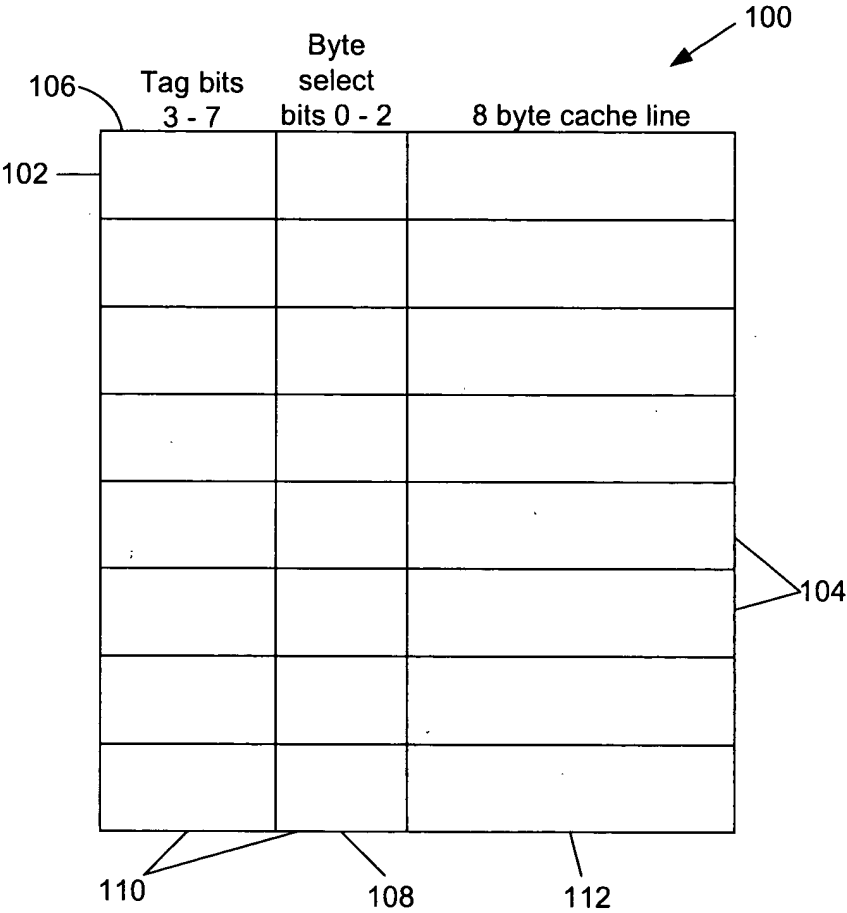


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4 - Way Set Associative Cache

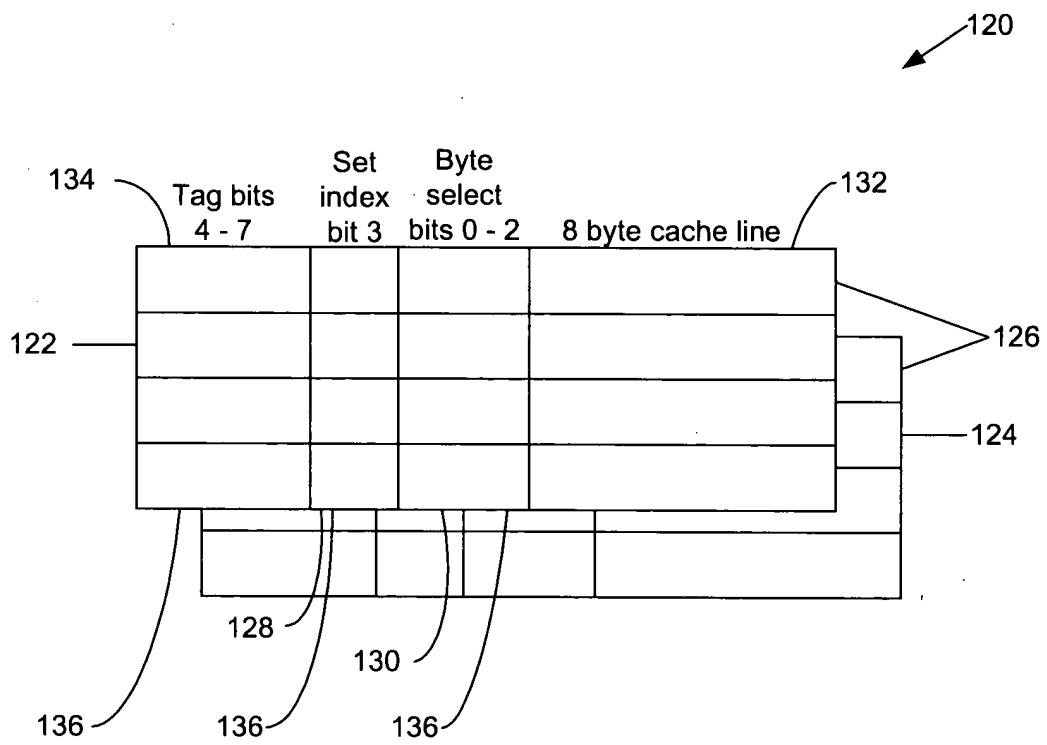


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Direct Mapped Cache

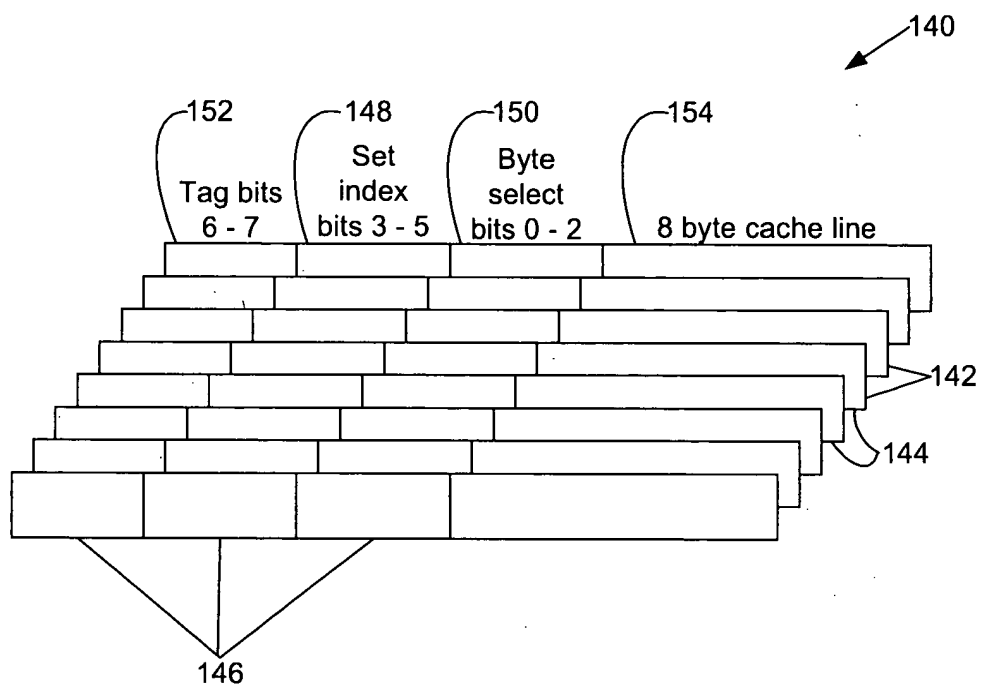


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Cache Configuration Device

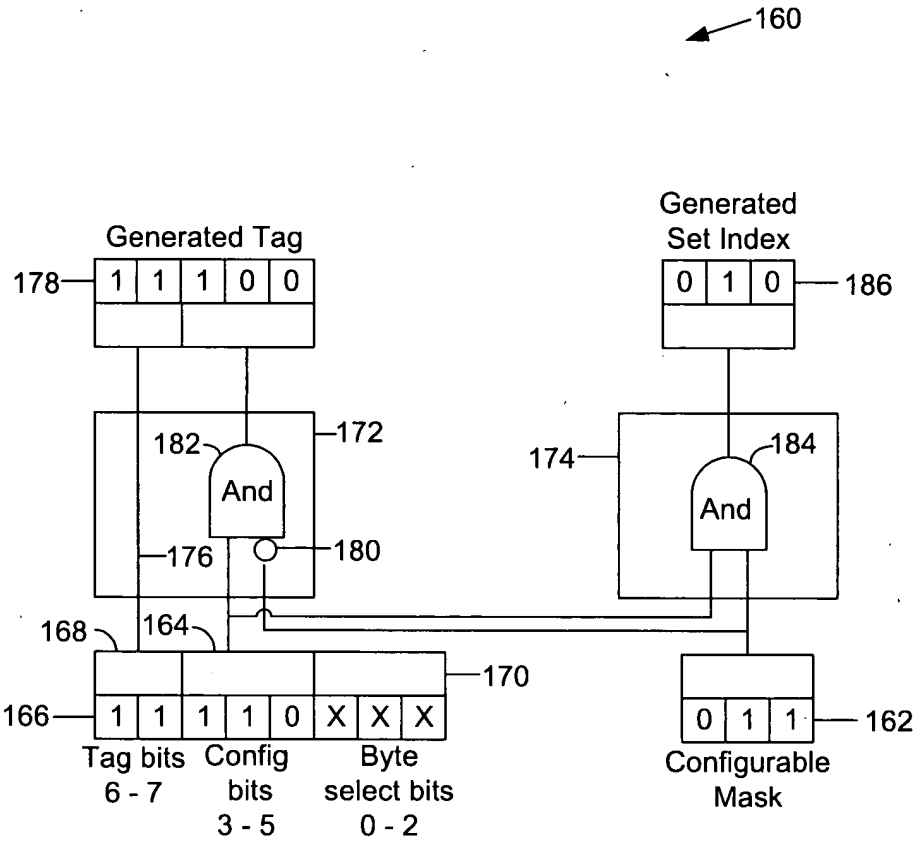
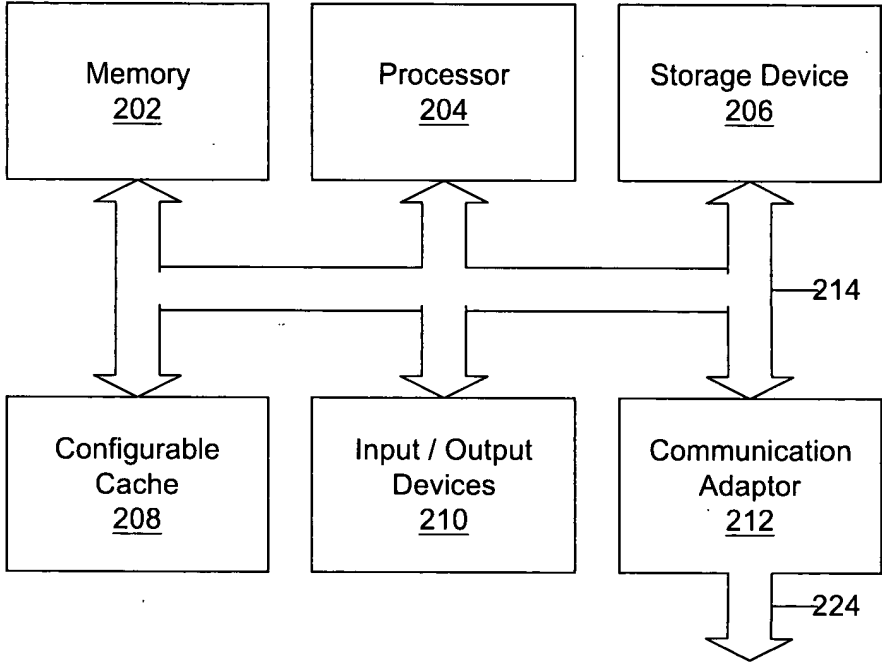


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Node Having
Configurable Cache

200



The diagram illustrates a node architecture. At the top, a title 'Node Having Configurable Cache' is centered. To the right, a reference numeral '200' with an arrow points to the overall node structure. The node is represented by a central horizontal bus labeled '214'. Above this bus are three components: 'Memory 202', 'Processor 204', and 'Storage Device 206'. Below the bus are three components: 'Configurable Cache 208', 'Input / Output Devices 210', and 'Communication Adaptor 212'. Bidirectional arrows connect each of these six components to the central bus. The 'Communication Adaptor 212' has a downward-pointing arrow labeled '224' extending from its bottom, representing an external connection.

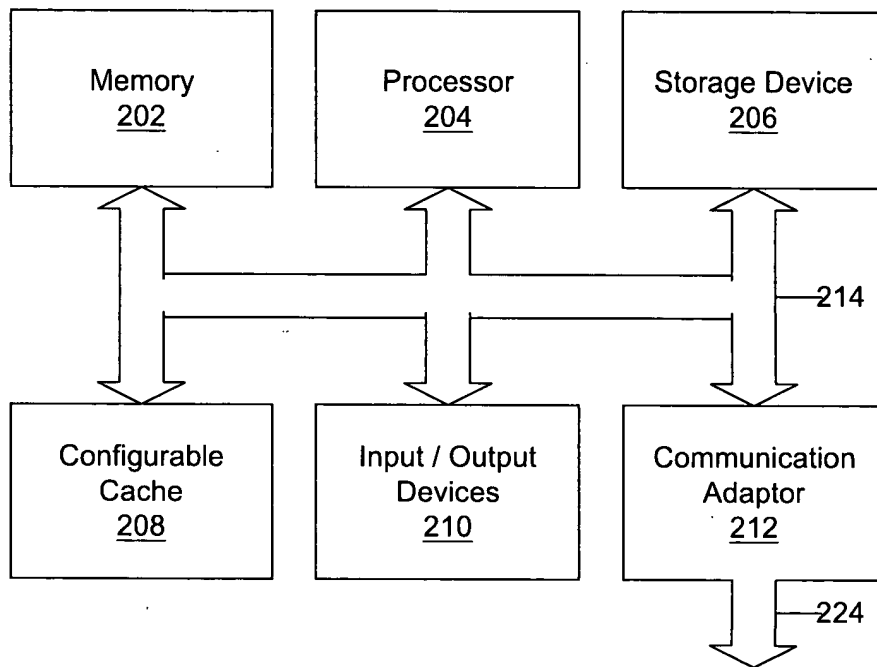


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System

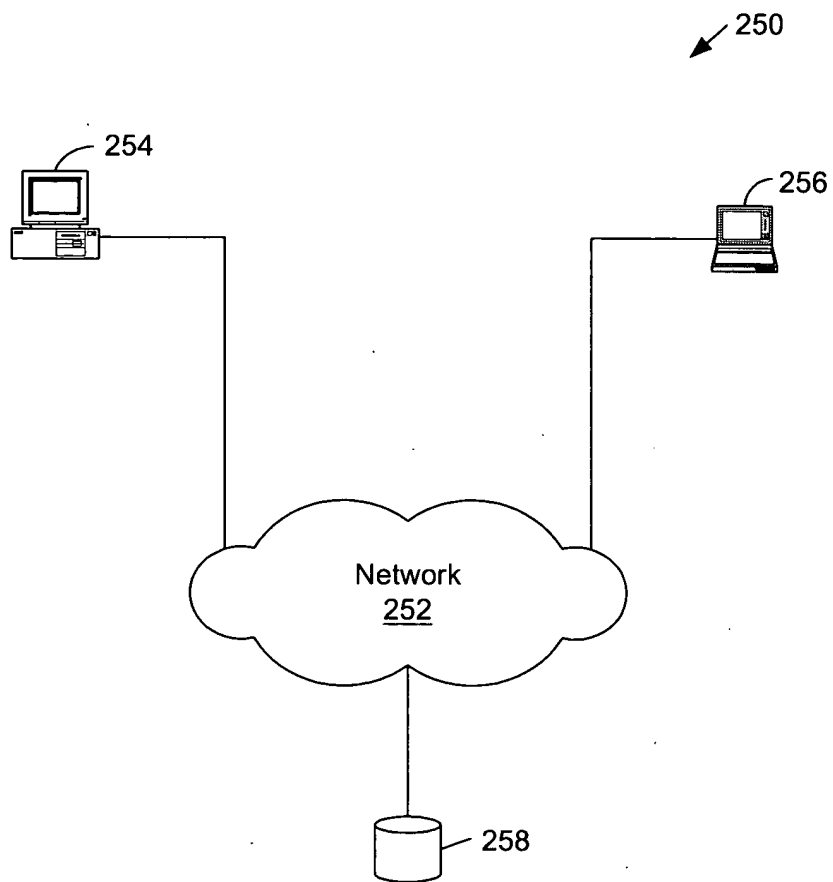


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